

Docket No.: 050099-0238



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
Shigenobu MAEDA, et al.	:	Confirmation Number: 6603
	:	
Application No.: 10/748,273	:	Group Art Unit: 2823
	:	
Filed: December 31, 2003	:	Examiner: Than V. Pham
	:	

For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE HAVING IMPURITY  
REGION UNDER ISOLATION REGION (As Amended)

**AMENDMENT FILED WITH REQUEST FOR CONTINUED EXAMINATION**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowance dated September 21, 2005, please amend the  
above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this  
paper.

**Remarks** begin on page 10 of this paper.